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Testing Digital Systems I

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This course examines in depth the theory and practice of fault analysis, test generation, and design for testability for digital ICs and systems.

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Each student is strongly urged to attend one of these two sessions each week, either remotely or in the classroom on the Carnegie-Mellon Pittsburgh campus. This course examines in depth the theory and practice of fault analysis, test generation, and design for testability for digital ICs and systems.

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